

CST Schematic Supported Board File Formats

User Guide

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Revision History

Document Number	Revision Number	Description	Revision Date
052515	1.0	Initial release	May 2015
052515	1.2	Document re- structure	Dec 2017
052515	1.3	Updated Cadence and Mentor Graphics support	Jan 2018
052515	1.4	Updated Mentor Graphics and Cadence formats	Feb 2018
052515	1.5	Updated Cadence Software Names	Feb 2018



1 SCT Supported Board File Formats

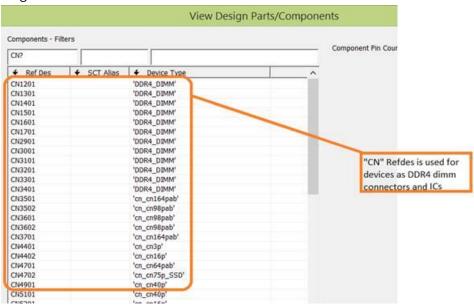
EDA Vendor	ECAD SW	SW version	Supported File Extension	Notes
Cadence	Allegro PCB Designer (Allegro PCB Editor)	16.6	.brd	Component placement or trace routing not required
	Allegro Design Entry CIS (OrCAD Capture CIS)	16.6 and 17.2	.iscf	iscf can be exported from a DSN.
	Allegro Design Entry HDL (Concept HDL Packaged Schematic Files)	16.6 and 17.2	pstxref.dat, pstchip.dat & pstxprt.dat	.dat files are exported from DSN file
Mentor Graphics	Xpedition and VX.x.x	v2005 EE2007.x, VX.x	.iscf	.iscf can be exported from DxDesigner
	DxDesigner Schematic	v2005 EE2007.x, VX.x	.iscf	.iscf can be exported from DxDesigner
	PADS		.iscf	.iscf can be exported from PADS
Zuken	CR-8000 Design Gateway Schematic	2012.100 and later	.iscf	iscf can be exported directly from. Design Gateway
	CR-5000 System Designer Schematic	13.0 and later	.iscf	.iscf can be exported directly from Design Gateway (support.zuken.com)

Notes:

- Please zip all files before uploading to your ticket.
- ISCF refers to Intel Schematic Checking Format, for instructions on how to export to .ISCF please contact CST.Services@intel.com.



Schematic connector or ICs prefixes starting with "CN": Reference designators starting with "CN" are not allowed for automated schematic review. The automation always sees any component which name starts with "CN" as a 2-pin device regardless of the real pinout, causing false violations and graphic viewer captures. If your design has these condition we request to rename them before sending for review. For example, see the image below:



Required Documents for Schematic Review:

1. PDF Schematic

Complete and searchable

Preferable if it is non-hierarchical. If only hierarchical is available there might be a delay in completion

2. Board File or ISCF Extract (See table above)

Use to extract Netlists for automation

Component placement or trace routing not required



2 Not Supported Board File Formats

- Mentor Board Station.
- Altium Designer.

Note: This document does not in any way constitute an Intel endorsement of a product or supplier.

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