

Intel® Core™ i5-3610ME Processor (PGA) and Mobile Intel® QM77 Express Chipset Development Kit

Quick Start Guide

January 2013

Document Number: 328077-002



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Contents

1	Introduction		
	1.1	Terminology	. 5
	1.2	Reference Documents	. 5
	1.3	Required Peripherals	. 5
	1.4	Flash BIOS to SPI using DEDI-PROG tool	. 6
	1.5	Jumper Power Up and Down	. 6
	1.6	Power Up and Down	. 6
Tables			
Table 1. Table 2. Table 3.	Termin Referer Jumper	ology nce Documents · list	. 5 . 5



Revision History

Date	Revision	Description	
November 2012	001	Initial release.	
January 2013	002	Updated Section 1.3	



1 Introduction

This document describes the required peripherals and the start up procedure for powering on and powering down the Intel® Core™ i5-3610ME Processor (PGA) and Mobile Intel® QM77 Express Chipset Development Kit. Refer to the Development Kit User Guide to understand the features and use of the Development Kit. Please read and refer back to the Safety Warning Flyer supplied with this customer reference board during set up and use of this device.

1.1 Terminology

Table 1. Terminology

Term	Description		
Dev Kits	Development Kits		

1.2 Reference Documents

Table 2. Reference Documents

Document	Document No./Location
Intel® Core™ i5-3610ME Processor (PGA) and Mobile Intel® QM77 Express Chipset Development Kit User Guide	328076

1.3 Required Peripherals

- · Development Kit board
- 2G DDR3 non-ECC SO-DIMM memory
- 150W AC to DC power adaptor
- SATA + power cable (custom made)
- Intel SSD Hard Disk

Intel® Core™ i5-3610ME Processor (PGA) and Mobile Intel® QM77 Express Chipset Development Kit January 2013 Quick Start Guide Document Number: 328077-002 5



1.4 Flash BIOS to SPI using DEDI-PROG tool

The Intel® Core™ i5-3610ME Processor (PGA) and Mobile Intel® QM77 Express Chipset Development Kit require a 2 partition SPI image for SPI-0 and SPI-1 respectively. The Descriptors sit on SPI-0 while the BIOS on SPI-1.

- 1. Ensure that power is applied to the Board.
- 2. Clip the Dedi-Prog SF100 with the testclip on top of the SPI chip.
- 3. SPI-0 refers to refdes U2E3 while SPI-1 refers to refdes U2E4 on the board.
- 4. Remove the Dedi-Prog connector once flashing is completed.

Note:

Flashing the BIOS on the Development Kit board will require re-programming of the onboard MAC Address.

1.5 Jumper

Caution: Do not move jumpers with the power on. Always turn off the power and unplug the power cord from the computer before changing jumper settings. Else, it may damage the board.

Table 3. Jumper list

No.	Refdes	Description	Default Settings
1	J1D3	Clear CMOS	1-X
2	J1D2	Clear ME	1-X
3	J1B1	ME Enabling	1-X
4	J1D1	EDP/LVDS Select	2-3
5	J4C1	LVDS Voltage select	1-2
6	J3F2	eDP Voltage select	1-2

NOTE: A jumper consists of two or more pins mounted on the motherboard. When a jumper cap is placed over two pins, it is designated as 1-2. When there are more than two pins on the jumper, the pins to be shorted are indicated as 1-2 (to short pin 1 to pin 2), or 2-3 (to short pin 2 to pin 3). When no jumper cap is to be placed on the jumper, it is designated as 1-X.

1.6 Power Up and Down

Powering up the board:

- 1. Connect the 12V Power Supply Brick. (Only use the brick supplied with the development kit).
- 2. Press the power button.
- 3. As the system boots, press F2 to enter the BIOS setup screen.

Intel® Core™ i5-3610ME Processor (PGA) and Mobile Intel® QM77 Express Chipset Development Kit Quick Start Guide

January 2013
6

Document Number: 328077-002

Introduction



- 4. Check time, date, and configuration settings. The default settings should be sufficient for most users with the exception of Intel SpeedStep® Technology. This feature is disabled by default and can be enabled in setup.
- 5. The PCIe hot plug support has to be enabled in setup if hot plug detect is required.
- 6. Save and exit the BIOS setup.
- 7. The system boots and is ready for use.

Powering down the board:

The Development Kits can power down in three ways:

- Use OS-controlled shutdown through the Windows Start menu (or equivalent)
- Press the power button to begin power-down.
- If the system is hung, it is possible to asynchronously shut the system down by holding the power button down continuously for 4 seconds.

Note: Option 3 is not recommended.

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Intel® Core™ i5-3610ME Processor (PGA) and Mobile Intel® QM77 Express Chipset Development Kit January 2013 Quick Start Guide Document Number: 328077-002 7